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INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANTS Daniel CHATROUX et al.			
				FILING DATE May 11, 2005			

  

U.S. PATENT DOCUMENTS						
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
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/SM/	2.	US 5,486,783	01/23/1996	BAUMERT et al.		
/SM/	3.	US 5,894,508	04/13/1999	KIM		
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/SM/	9.	EP 0 638 858 A1	02/15/1995	EUROPE		

  

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/SM/	10. S.C. Tan et al.; "Low Power CMOS Level Shifters by Bootstrapping Technique"; Electronic Letters, IEE Stevenage, GB, Vol. 38, No. 16, pages 876-878; August 1, 2002

  

EXAMINER /Suresh Memula/	DATE CONSIDERED 04/25/2007
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Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Date: May 11, 2005

<b>Notice of References Cited</b>	Application/Control No. 10/534,462		Applicant(s)/Patent Under Reexamination CHATROUX ET AL.	
	Examiner Suresh Memula		Art Unit 2825	Page 1 of 1

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**NON-PATENT DOCUMENTS**

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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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